

## **REMARKS/ARGUMENT**

Applicant responds herein to the Office Action dated June 29, 2004.

### **Regarding the Amendments to the Specification:**

Several typographical and/or translation errors which were noted in the specification during preparation of this response have been corrected. No new matter has been introduced by these amendments.

### **Regarding the Claims in General:**

Claims 1-15 remain pending. Claims 1, 2, 8-10, and 14 have been amended to improve the form thereof, but without any change in scope.

### **Regarding The Allowable Subject Matter**

Applicant notes with appreciation the indication that claims 2, 9, and 14 would be allowed if rewritten in independent form incorporating the limitations of their respective parent claims. Because these claims are all dependent on claim 1 or 8, both of which are believed to be allowable for the reasons stated below, claims 2, 9, and 14 have been retained in dependent form pending the Examiner's further consideration.

### **Regarding the Prior Art Rejections:**

In the outstanding Office Action, claims 1, 3-8, 10-13, and 15 were rejected as anticipated by Funaba et al. U.S. Patent 6.411.539 (Funaba). Applicant respectfully traverses this rejection.

Preliminarily, the Examiner's attention is respectfully directed to the fact that Funaba is assigned to Hitachi, Ltd. The present application is assigned to Elpida Memory, Inc.

Considering now the rejection, the Examiner refers to Fig. 11 of Funaba as purporting to show all the features of independent claims 1 and 8. However, both of these claims specify that the memory module comprises:

- a memory chip;
- a pin configured and positioned to engage with the connector;

a bus which connects said memory chip to said pin;  
a terminating resistor connected to one end of said bus, and  
a stab resistor connected between said pin and the other end of said bus.

Consideration of Fig. 11 of Funaba readily reveals that both the buses and the terminal resistors are *on the motherboard*, and not part of the memory modules. Thus, Fig. 11 of Funaba discloses a memory module which comprises only the claimed memory chips and stab resistors, and therefore does not anticipate either independent claim 1 or 8.

Rejected claims 3-7 and 15 are dependent on allowable claim 1, and claims 10-13 are dependent on allowable claim 8, and are therefore allowable for the reasons stated above. In addition, these claim recite features which, in combination with the features of their respective parent claims are neither taught nor suggested in the Funaba patent.

#### **Regarding Other References Cited by the Examiner**

The other references cited by the Examiner but not applied have been considered, but the present claims are not believed to be anticipated or rendered obvious by these references.

Accordingly, the Examiner is respectfully requested to reconsider the application, allow the claims as amended and pass this case to issue.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 27, 2004

Max Moskowitz

Name of applicant, assignee or  
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Signature

August 27, 2004

Date of Signature

Respectfully submitted,

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